



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Kenji MORITA, et al.

Appln. No.: 09/843,649

Group Art Unit: 2622

Confirmation No.: 7847

Examiner: Not Yet Assigned

Filed: April 27, 2001

For: IMAGE PROCESSING DEVICE AND IMAGE DATA CONVERSION METHOD

**SUBMISSION OF FORMAL DRAWINGS**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Submitted herewith please find ten (10) sheets of formal drawings. The Examiner is respectfully requested to acknowledge receipt of these formal drawings.

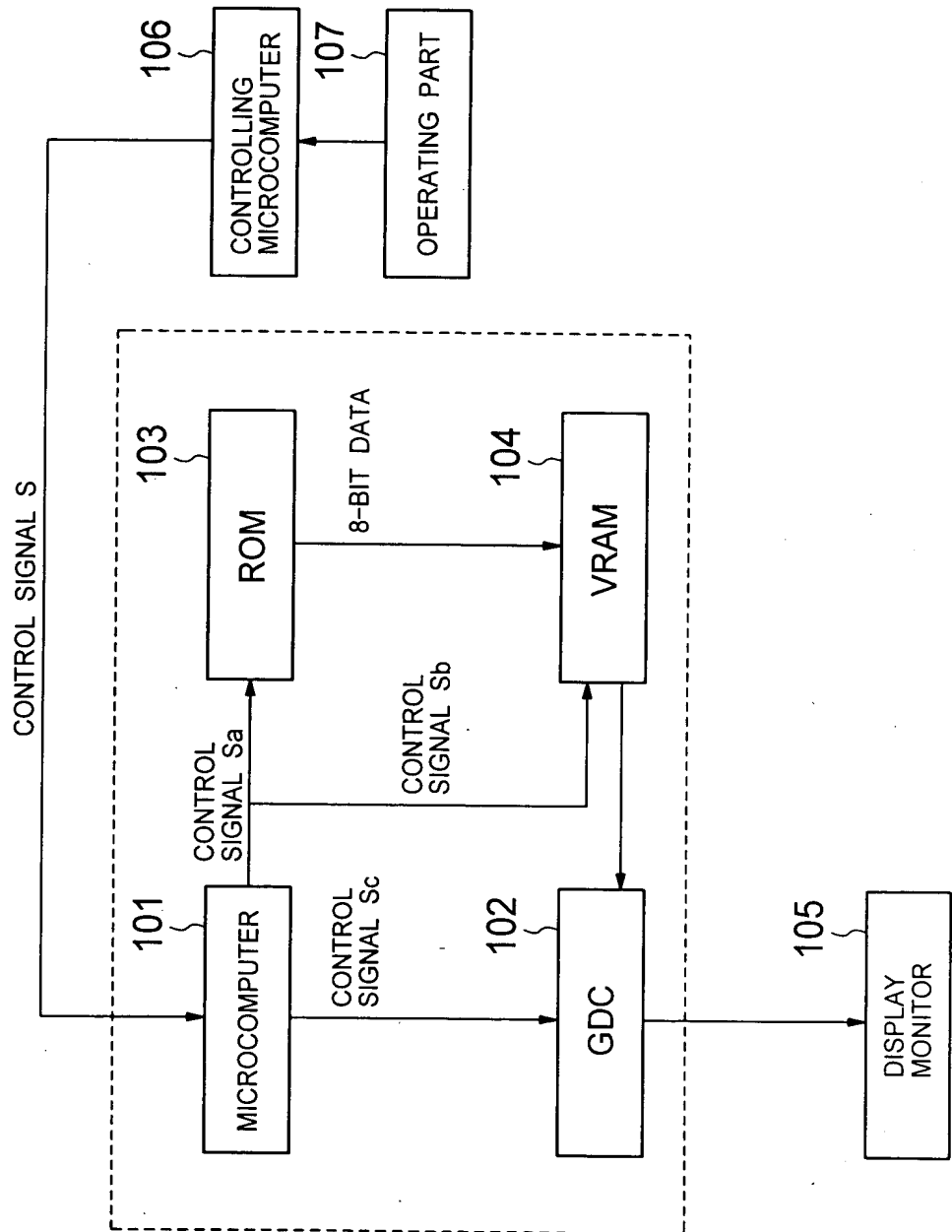
Respectfully submitted,

SUGHRUE, MION, ZINN,  
MACPEAK & SEAS, PLLC  
2100 Pennsylvania Avenue, N.W.  
Washington, D.C. 20037-3213  
Telephone: (202) 293-7060  
Facsimile: (202) 293-7860  
DM/tmm  
Date: August 21, 2001

  
Darryl Mexit  
Registration No. 23,063



FIG. 1  
 (PRIOR ART)



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FIG. 2

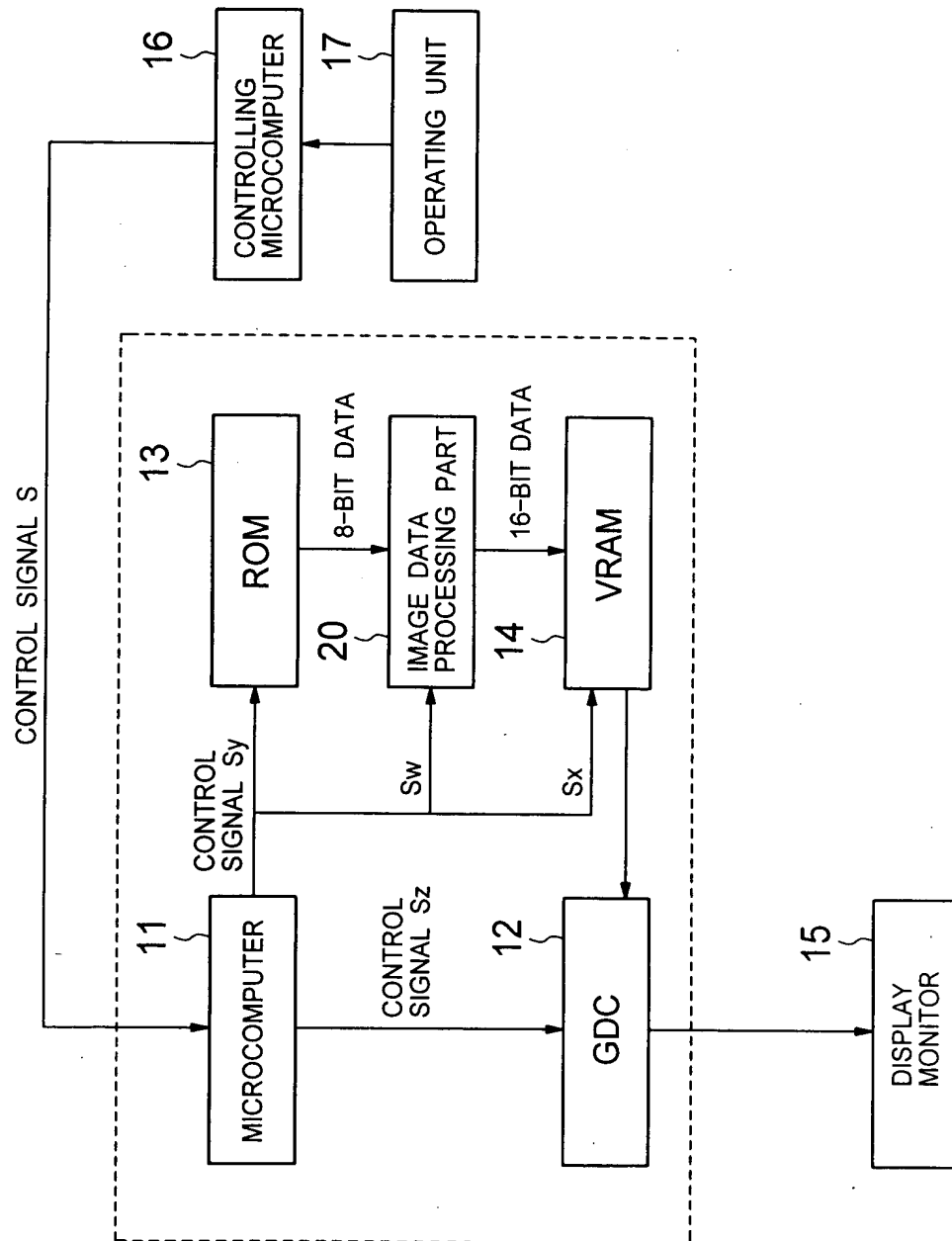
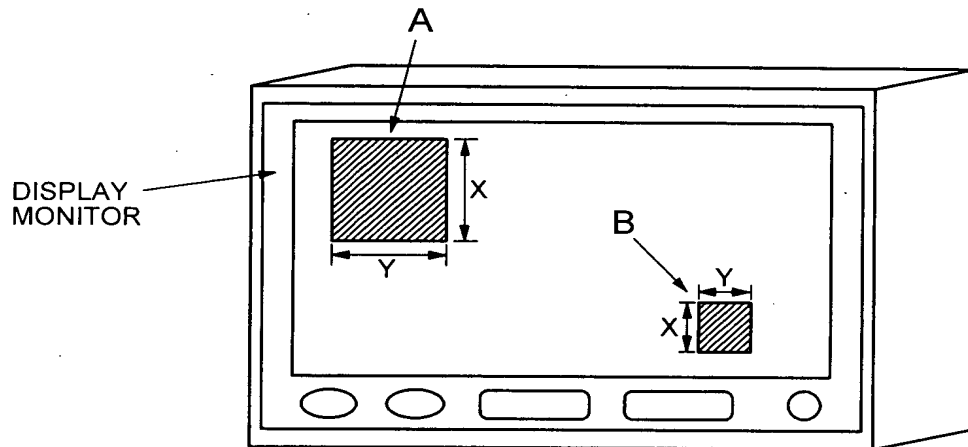




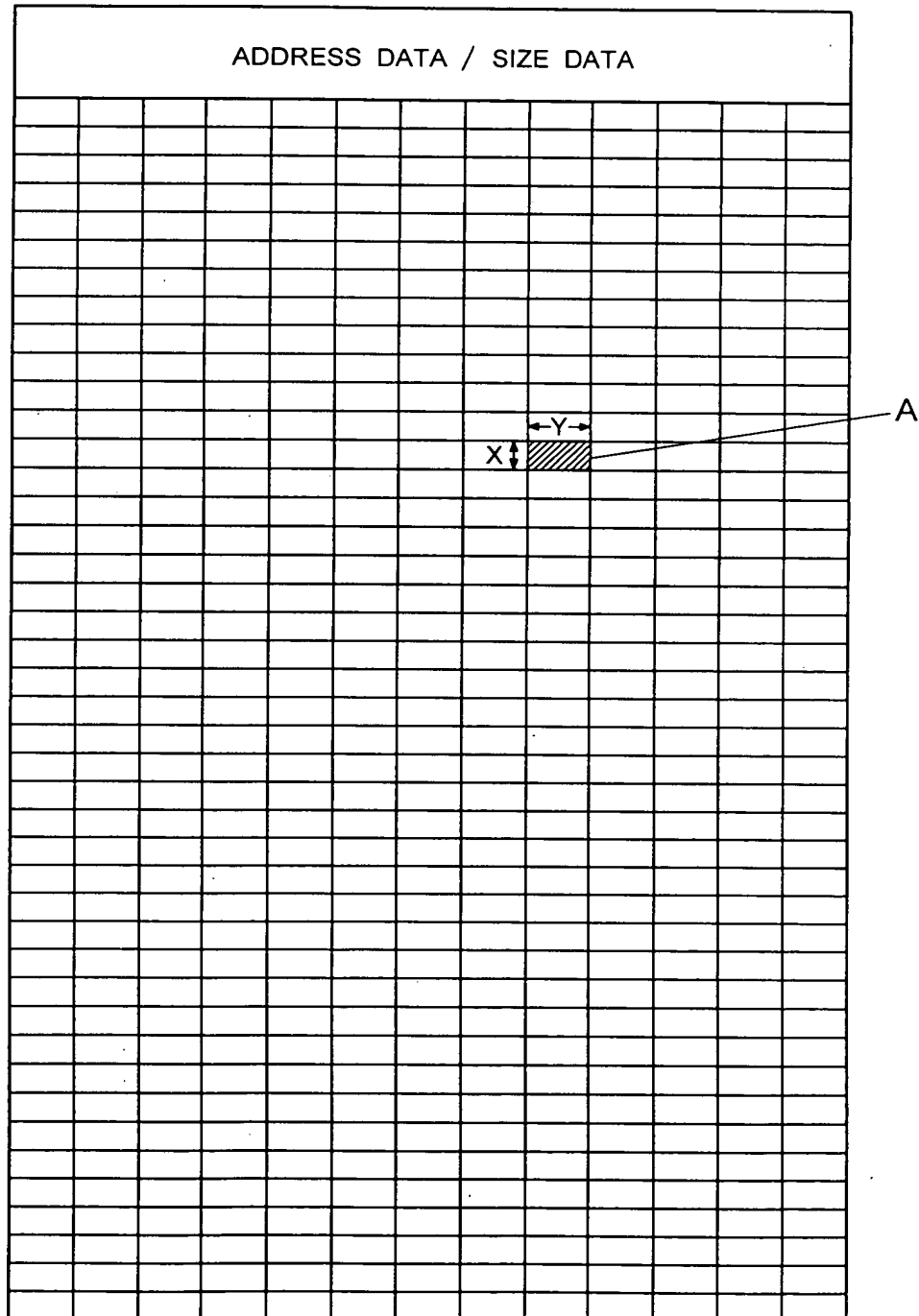
FIG. 3



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FIG. 4



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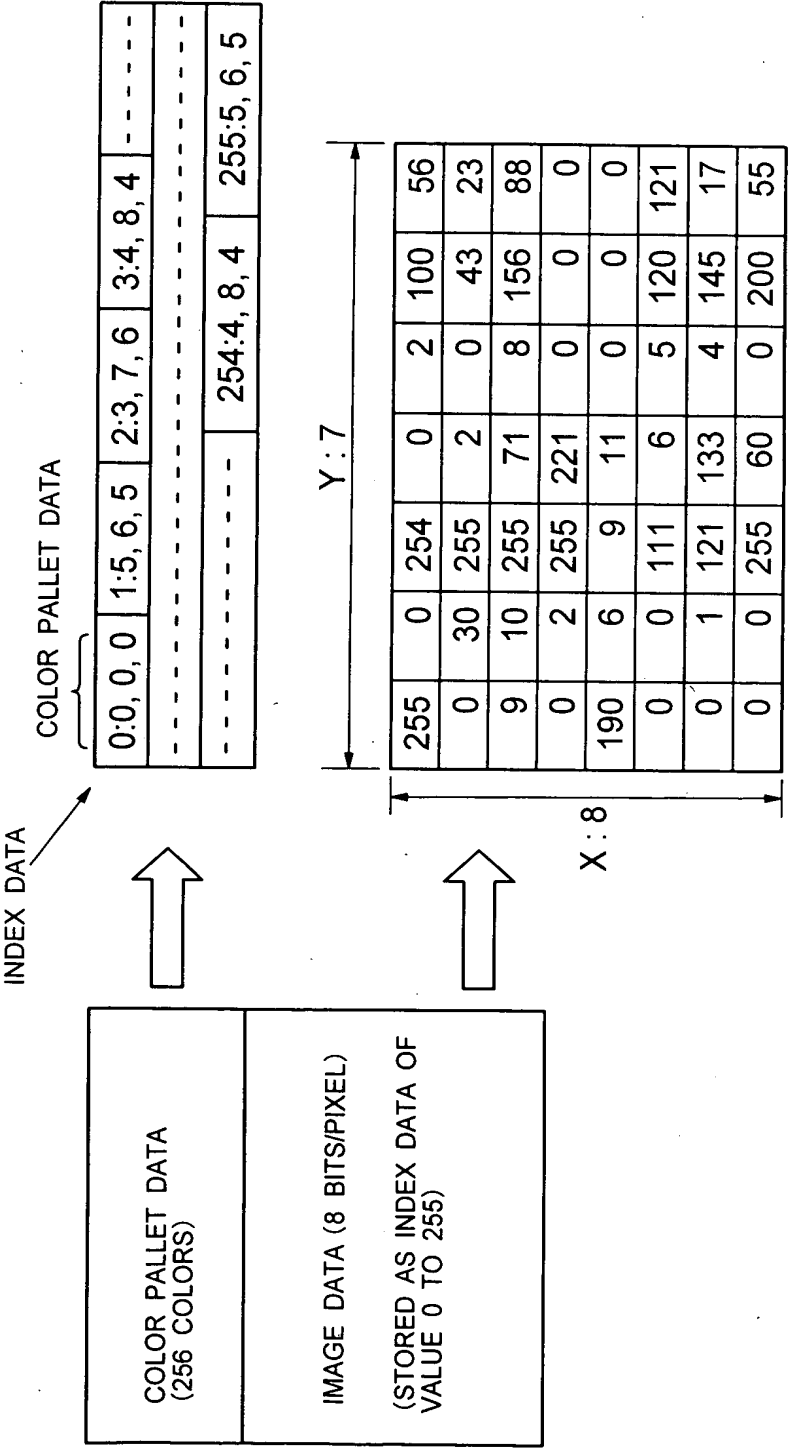


FIG. 5

ADDRESS DATA	1	2	3	---
SIZE DATA	X : 100, Y : 200	X : 240, Y : 400	X : 90, Y : 150	---
---	---	---	---	---
---	---	---	---	---
---	---	1608	1609	---
---	---	X : 100, Y : 300	X : 150, Y : 200	---



FIG. 6



**INDEX DATA  
IN ROM**

**COLOR PALETTE  
DATA**

0,0,0	5,6,5	3,2,6	0,0,0	5,7,4	3,9,4
0,0,0	4,8,4	0,0,0	5,8,3	9,1,6	0,0,0
0,0,0	- - - -	- - - -	- - - -	- - - -	- - - -
- - - -	- - - -	- - - -	- - - -	- - - -	- - - -
- - - -	- - - -	- - - -	- - - -	- - - -	- - - -
- - - -	- - - -	- - - -	- - - -	- - - -	- - - -
- - - -	- - - -	- - - -	- - - -	- - - -	- - - -
- - - -	- - - -	- - - -	- - - -	- - - -	- - - -
1,9,6	7,5,4	6,5,5	8,2,6	0,0,0	4,5,7

**DATA STRUCTURE OF VRAM**

**1 PIXEL**

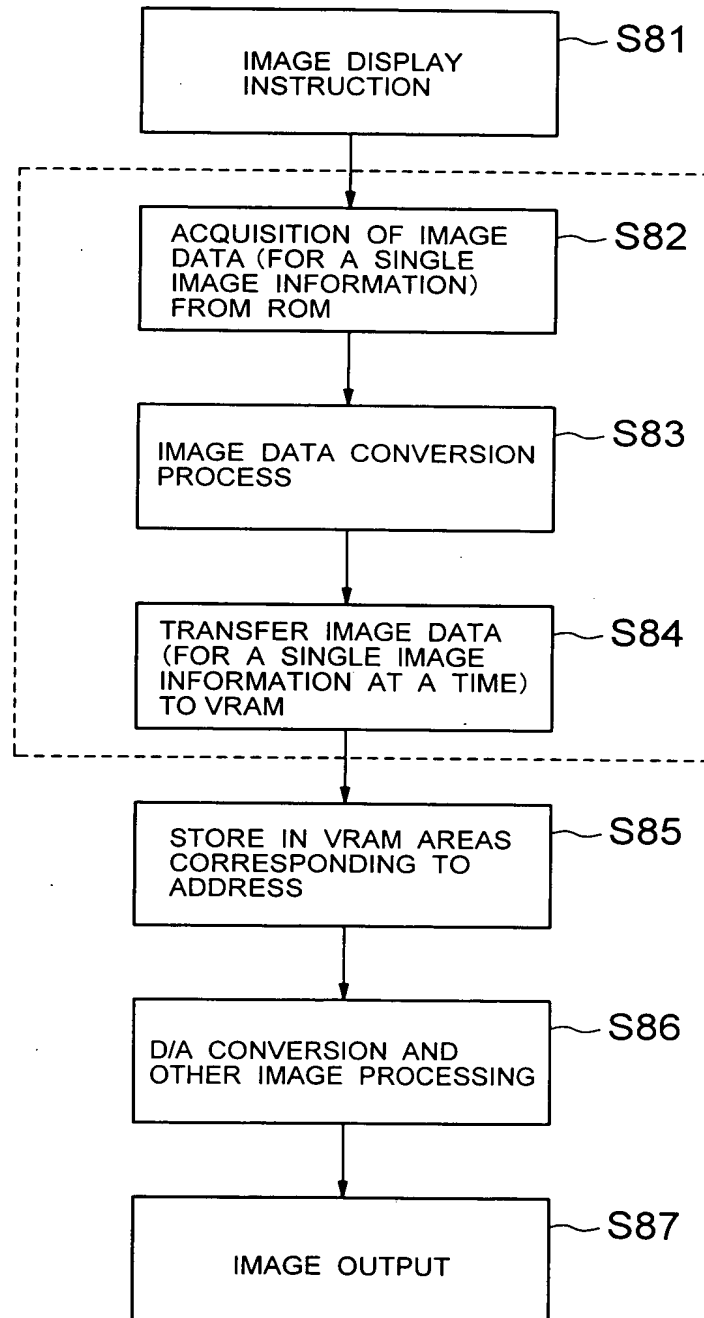


FIG. 8

[illegible]



FIG. 9



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FIG. 10

